



BAT54VY

Schottky barrier triple diode

20 April 2023

Product data sheet

1. General description

General-purpose Schottky, triple diode in a SOT363 ultra small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low forward voltage
- Low capacitance
- Ultra small and flat lead SMD plastic package
- Flat leads: excellent coplanarity and improved thermal behavior

3. Applications

- Ultra high-speed switching
- Voltage clamping
- Line termination
- Reverse polarity protection

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per diode						
I_R	reverse current	$V_R = 25\text{ V}; T_{\text{amb}} = 25\text{ °C}$	-	-	2	μA
V_R	reverse voltage		-	-	30	V

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode (diode 1)	<p>TSSOP6 (SOT363)</p>	<p>aaa-005704</p>
2	A2	anode (diode 2)		
3	A3	anode (diode 3)		
4	K3	cathode (diode 3)		
5	K2	cathode (diode 2)		
6	K1	cathode (diode 1)		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BAT54VY	TSSOP6	plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	SOT363

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BAT54VY	K9%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per diode					
V_R	reverse voltage		-	30	V
I_F	forward current		[1] [2]	200	mA
I_{FRM}	repetitive peak forward current	$t_p \leq 10 \text{ ms}$; $\delta \leq 0.5$	-	900	mA
I_{FSM}	non-repetitive peak forward current	$t_p = 50 \mu\text{s}$; square wave; $T_{j(\text{init})} = 25 \text{ }^\circ\text{C}$	-	11	A
		$t_p = 10 \text{ ms}$; square wave; $T_{j(\text{init})} = 25 \text{ }^\circ\text{C}$	-	1.5	A
Per device; one diode loaded					
T_j	junction temperature		-	150	$^\circ\text{C}$
T_{amb}	ambient temperature		-55	150	$^\circ\text{C}$
T_{stg}	storage temperature		-65	150	$^\circ\text{C}$

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Single diode loaded.

9. Thermal characteristics

Table 6. Thermal characteristics

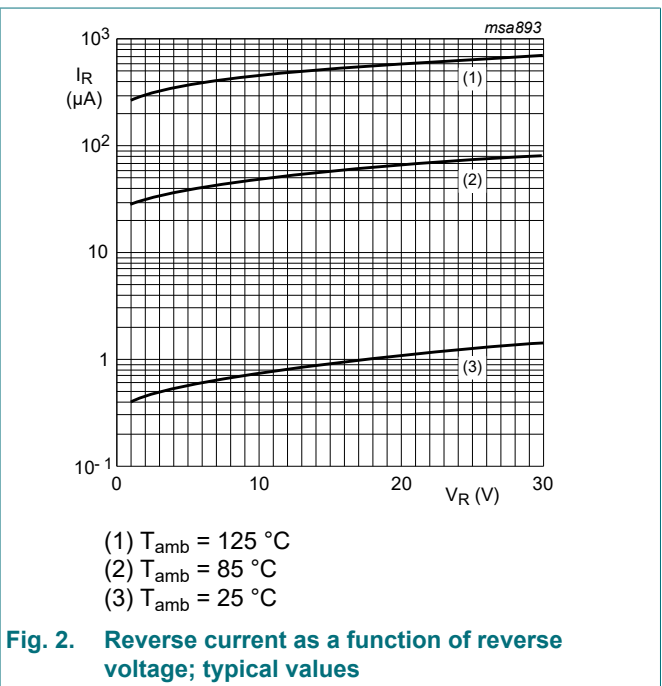
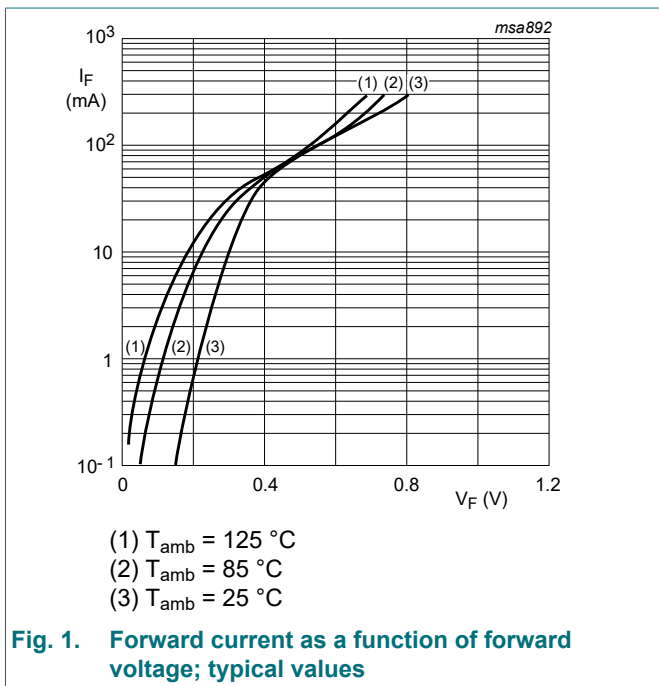
Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	490	K/W
			[2]	-	-	430	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[3]	-	-	150	K/W

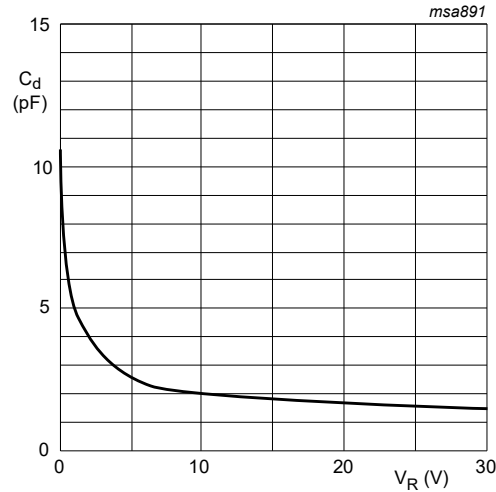
- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [3] Soldering points at pins 4, 5 and 6.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per diode						
V_F	forward voltage	$I_F = 0.1 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	240	mV
		$I_F = 1 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	320	mV
		$I_F = 10 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	400	mV
		$I_F = 30 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	500	mV
		$I_F = 100 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	800	mV
I_R	reverse current	$V_R = 25 \text{ V}$; $T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	2	μA
C_d	diode capacitance	$V_R = 1 \text{ V}$; $f = 1 \text{ MHz}$; $T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	10	pF





f = 1 MHz; T_{amb} = 25 °C

Fig. 3. Diode capacitance as a function of reverse voltage; typical values

11. Package outline

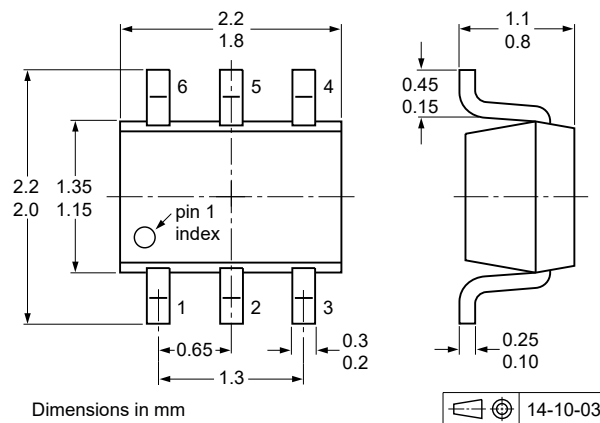


Fig. 4. Package outline TSSOP6 (SOT363)

12. Soldering

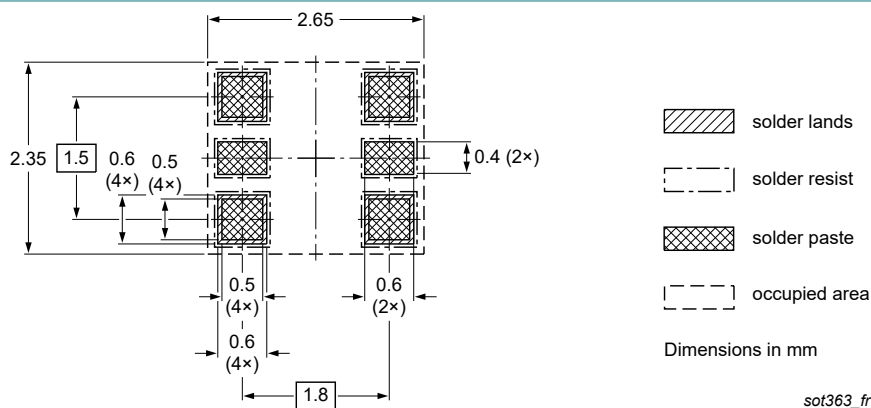


Fig. 5. Reflow soldering footprint for TSSOP6 (SOT363)

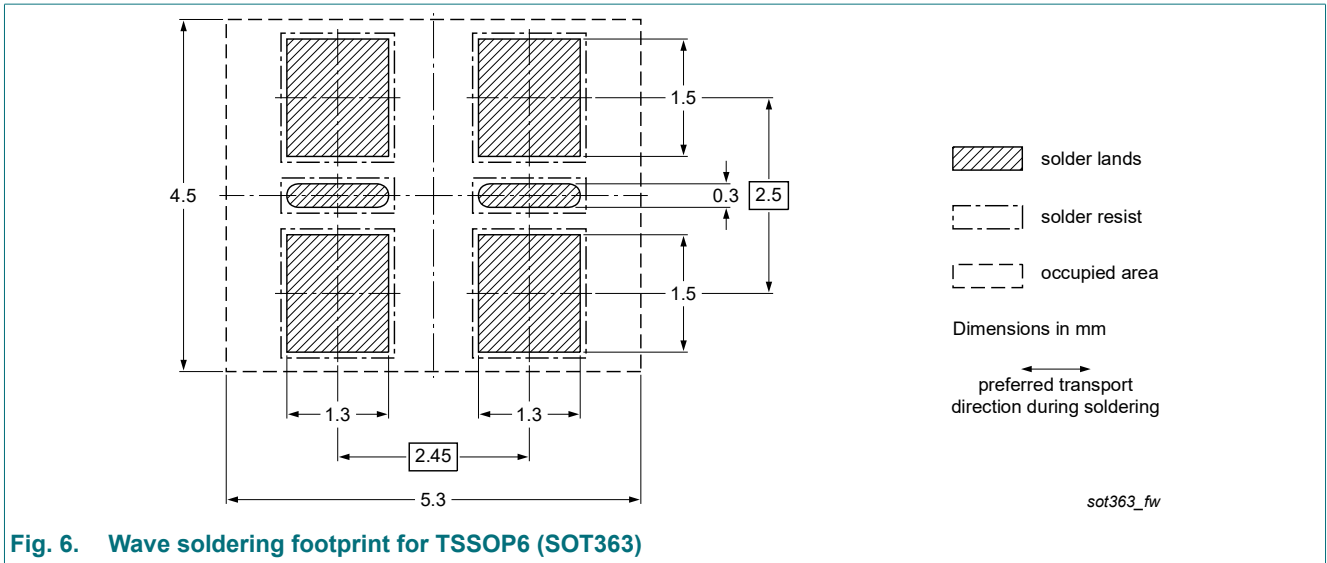


Fig. 6. Wave soldering footprint for TSSOP6 (SOT363)

13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BAT54VY v.1	20230420	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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