

- iEthernet
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- > W5100S
- > W5500
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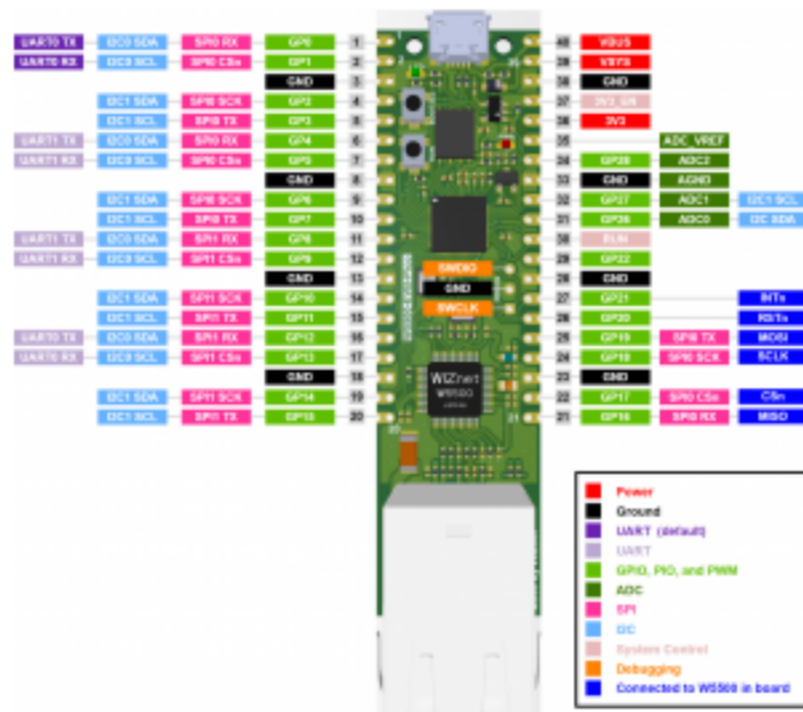
W5500-EVB-Pico

W5500-EVB-Pico is a microcontroller evaluation board based on the Raspberry Pi RP2040 and fully hardwired TCP/IP controller W5500 – and basically works the same as Raspberry Pi Pico board but with additional Ethernet via W5500.

Key Features

- RP2040 microcontroller with 2MByte Flash
- Dual-core cortex M0+ at up to 133MHz
- 264kByte multi-bank high performance SRAM
- External Quad-SPI Flash with eExecute In Place (XIP)
- High performance full-crossbar bus fabric
- 30 multi-function General Purpose IO (4 can be used for ADC)
 - 1.8-3.3V IO Voltage (NOTE. Pico IO voltage is fixed at 3.3V)
 - 12-bit 500kps Analogue to Digital Converter (ADC)
- Various digital peripherals
 - 2 × UART, 2 × I2C, 2 × SPI, 16 × PWM channels
 - 1 × Timer with 4 alarms, 1 × Real Time Counter
- 2 × Programmable IO (PIO) blocks, 8 state machines total
- Flexible, user-programmable high-speed IO
- Can emulate interfaces such as SD Card and VGA
- Includes W5500
 - Supports Hardwired Internet Protocols: TCP, UDP, ICMP, IPv4, ARP, IGMP, PPPoE
 - Supports 8 Independent Hardware SOCKETs simultaneously
 - Internal 32 Kbytes Memory for TX/ RX Buffers
 - Supports High Speed Serial Peripheral Interface(SPI MODE 0, 3)
 - Micro-USB B port for power and data (and for reprogramming the Flash)
 - 40 pin 21×51 'DIP' style 1mm thick PCB with 0.1" through-hole pins also with edge castellations
 - 3-pin ARM Serial Wire Debug (SWD) port
 - 10 / 100 Ethernet PHY embedded
 - Supports Auto Negotiation
 - Full / Half Duplex
 - 10 / 100 Based
 - Built-in RJ45
 - Built-in LDO

Hardware Specification



W5500-EVB-Pico



W5500



Technical Docs & Driver



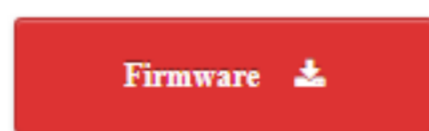
Technical Support



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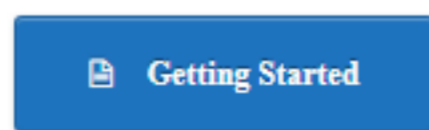
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Materials in WIZwiki



W5500-EVB-Pico Firmware and Example

- Firmware components
- Drivers
- Application Projects



Getting Started

- User guide for W5500-EVB-Pico

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